Release Notes

SDCC Compiler Tool chain for XC800

The SDCC for XC800 is the port of the free SDCC compiler 2.4.0. Although, originally it was 2.4.0, this was later updated to the 2.5.4 March 2006 nightly version. The port for Infineon Technologies 8 bit microcontroller XC800 series is versioned as 2.5.4B4. It is under GPL but has not yet been put into FSF. This SDCC release for XC800 is a Beta4 Release. This software is of beta quality and tested with the XC856, XC886/888 and XC866 boards.

This “Release Notes” covers Release Information, Released items, the important features, list of closed and open bugs, limitations, system requirements, and download and installation instructions.

Release Information:

Release Version No	Post M8 Intermediate Release R1.6 (Version 2.5.4 B4)
Date of Release	Dec 15th 2006
Description of known Open Bugs	• Some issues related to large values (boundary values) of floating point numbers exist. – Comparisons between floating point numbers which are very close may result in erroneous output.

Released Items:

S. No.	Released Item	Version	Status of Update
1	SDCC Toolchain for XC800 (SDCC_XC800_2.5.4B4_R1.6_Setup.zip)	2.5.4 B4	Updated
2	User Manual for SDCC Compiler for XC800 (sdcc_xc800_usermanual.pdf)	2.5.4 B4	Updated
3	Release Notes (SDCC_XC800_CompilerToolchain_Release_Notes.pdf)	2.5.4 B4	Updated

Important Features:

This is a Beta4 Release of the software (version is 2.5.4 B4) to support XC856 Board. The package continues the support for XC866 and XC886/888 without any change to the existing interface.

System Requirements:

SDCC requires Windows NT/Windows2000/ Windows XP based machines with minimum 64MB RAM.

Installation Instructions:

Follow the instructions in the installer.

Changes Over Previous Version:

- Support for XC856 is added. (Header file xc856.h is added)
- User Manual is Updated.

Limitations:

- The compiler still does not support generation of code using two data pointers (DPSEL register). The assembler identifies DPSEL register and generates the code accordingly.
- Compiler does not generate code for the new variant of MOVC instruction. This is supported by the macro defined in the xc866.h file.
- TRAP instruction is emitted only by the macro defined in the xc866.h file.
- Extern variables cannot be assigned before its extern declaration.
- Programs requiring dynamic memory allocation using “malloc()” should be compiled in model large.
- No support for signed bit fields.

Known Issues(Other than open bugs):

No known issues.

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